# 2SJ0364 (2SJ364)

### Silicon P-Channel Junction FET

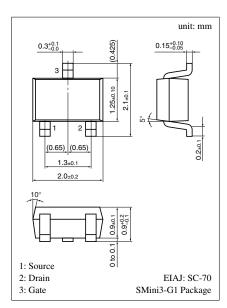
#### For analog switch

#### ■ Features

- Low ON-resistance
- Low-noise characteristics

#### ■ Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Gate to Drain voltage	V <sub>GDS</sub>	65	V
Drain current	$I_{\mathrm{D}}$	-20	mA
Gate current	$I_{G}$	-10	mA
Allowable power dissipation	$P_{\mathrm{D}}$	150	mW
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C



Marking Symbol (Example): 4M

#### ■ Electrical Characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I <sub>DSS</sub> *	$V_{DS} = -10V, V_{GS} = 0$	- 0.2		-6	mA
Gate to Source leakage current	$I_{GSS}$	$V_{GS} = 30V, V_{DS} = 0$			10	nA
Gate to Drain voltage	V <sub>GDS</sub>	$I_{G} = 10\mu A, V_{DS} = 0$	65			V
Gate to Source cut-off voltage	V <sub>GSC</sub>	$V_{DS} = -10V, I_{D} = -10\mu A$		1.5	3.5	V
Forward transfer admittance	Y <sub>fs</sub>	$V_{DS} = -10V, I_{D} = -1mA, f = 1kHz$	1.8	2.5		mS
Drain to Source ON-resistance	R <sub>DS(on)</sub>	$V_{DS} = -10 \text{mV}, V_{GS} = 0$		300		Ω
Input capacitance (Common Source)	C <sub>iss</sub>	V - 10V V - 0 f - 1MHz		12		pF
Reverse transfer capacitance (Common Source)	C <sub>rss</sub>	$V_{DS} = -10V, V_{GS} = 0, f = 1MHz$		4		pF

 $<sup>^{\</sup>ast}$   $I_{DSS}$  rank classification

Runk	О	P	Q	R
I <sub>DSS</sub> (mA)	− 0.2 to −1	- 0.6 to -1.5	−1 to −3	−2.5 to −6
Marking Symbol	4MO	4MP	4MQ	4MR

Note) The part number in the parenthesis shows conventional part number.

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1.5 1.0

0.5

1.5

1.0

Gate to source voltage  $V_{GS}$  (V)

0.5

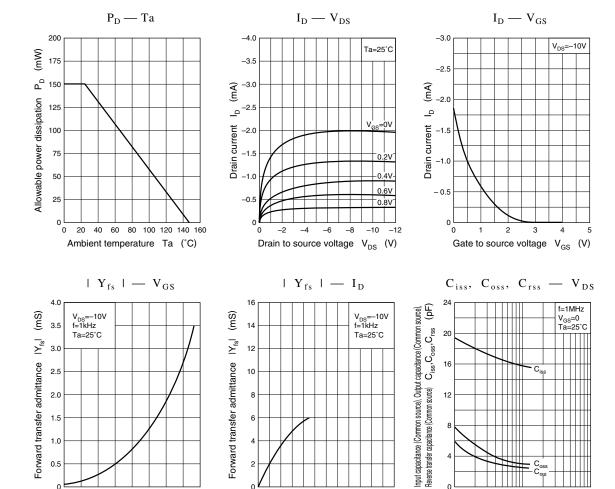
-3

-10

Drain to source voltage  $\,V_{\rm DS}\,\,$  (V)

-30

-100



2

0 K

-6

Drain current I<sub>D</sub> (mA)

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